



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Enclosures:

Applicant : Glenn J. Leedy

Application No. : 10/665,757

Confirmation No. : 6828

Filed : September 19, 2003

For : METHOD OF MAKING AN INTEGRATED

CIRCUIT

Examiner Pamela E. Perkins

Art Unit : 2822

EXPRESS MAIL CERTIFICATION

EXPRESS MAIL LABEL NO. EV669680658US

Date of Deposit: May 4, 2006.

I hereby certify that this paper/fee is being deposited with the United States Postal Service "EXPRESS MAIL POST OFFICE TO ADDRESSEE" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to Mail Stop ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

isatu

- Transmittal Letter (3 pp. in duplicate);
- 3. One (1) Replacement Sheet of Formal Drawings
 (Sheet 4 of 64);
- 4. One (1) Annotated Sheet of Formal Drawings (Sheet 4 of 64); and
- 5. Return Postcard.